

AMENDMENTS TO THE SPECIFICATION

Please rewrite the paragraph bridging Pages 5 and 6 of the Specification, i.e., beginning on Page 5, line 14, through Page 6, line 23, as follows:

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“Fig. 1 is a block diagram showing the system configuration of a digital still camera as an image pickup apparatus according to an embodiment of the present invention. Referring to Fig. 1, this camera is comprised of an optical system 1 including a lens and the like, a photoelectric conversion element (CDD in this embodiment) 2 for converting an optical signal from the optical system 1 into an electrical signal, a CDS (Correlation Double Sampling) circuit 3 for converting an output signal from the CCD 2 into a video signal, an A/D converter 4 for converting an analog signal output from the CDS circuit 3 into a digital signal, a memory controller 5 for controlling a memory 9 (to be described later), a system controller 6 for generating a signal for driving a CCD driver (not shown) and controlling the memory controller 5, a D/A converter 7 for converting a digital signal output from the memory controller 5 into an analog signal, a display unit 8 such as a color liquid crystal display for displaying data, picked image, and the like, the memory 9 such as a RAM used to store image data, a compression circuit 10 for compressing an image signal by a compression scheme such as JPEG, a DSP (Digital Signal Processor) 11 for performing various signal processes such as a color signal processing and luminance signal processing, an

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used

EEPROM (Electrically Erasable Programmable Read Only Memory) ~~12~~
~~used to hold~~ 17 used to hold a white balance coefficient (WBstd) for a
standard light source, a white balance coefficient (WBf1) ~~for first strobe~~
for a first strobe light, a white balance coefficient (WBf2) ~~for second~~
~~strobe~~ for a second strobe light, and the like, a card memory 13 in which
image-picked data and the like are stored, a release switch 14 as a two-
stroke switch which is depressed to the first stroke to turn on the first
switch SW1 and is depressed to the second stroke to turn on the second
switch SW2, and a strobe control circuit 15 for controlling a strobe (not
shown).
